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Abstract of Disclosure

In each pixel region of a liquid crystal panel of an in plane switching mode, a pixel electrode is made to have a two-layer structure composed of a lower pixel electrode located in the same layer as a signal line and of an upper pixel electrode located in the same layer as a common electrode. Moreover, an electrode portion of the lower pixel electrode is made closer to the signal line than the upper pixel electrode. Furthermore, the signal line, a gate line and the common electrode are arranged in an overlapped manner.

Figures

Figure 1: A line graph showing the relationship between the number of hours spent studying and the score on a test. The x-axis represents the number of hours (0 to 10), and the y-axis represents the score (0 to 100). The data points are as follows:

Hours	Score
0	50
1	55
2	60
3	65
4	70
5	75
6	80
7	85
8	90
9	95
10	100

The graph shows a positive linear relationship between the number of hours spent studying and the score on the test. The line starts at (0, 50) and ends at (10, 100).